

CS_275 Automata and Formal Language Theory

Course Notes

Additional Material

(This material is no longer taught and not exam relevant)

Part III: Limits of Computation

Chapt. III.3: Turing Machines

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May 5, 2017

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Chapt. III.3 (Additional Material)

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III.3 (a) Definition of the Turing Machine

No Additional Material

For this subsection no additional material has been added yet.

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Sect. III.3 (a)

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III.3 (a) Definition of the Turing Machine

III.3 (b) Equivalence of URM computable and Turing computable functions

III.3 (c) Undecidability of the Turing Halting Problem

III.4 The Church-Turing Thesis

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Sect. III.3 (a)

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III.3 (b) Equivalence of URM computable and Turing computable functions

III.3 (a) Definition of the Turing Machine

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Sect. III.3 (b)

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Formal Lemma URM-computable \Rightarrow TM-computable

Lemma (3.4)

If $f : \mathbb{N}^n \rightarrow \mathbb{N}$ is URM-computable then it is Turing-computable by a TM with alphabet $\{0, 1, \sqcup\}$.

Remark

The proof that every Turing computable function is URM computable will not be given in this Section.
(It could be done directly. A much nicer argument which makes use of the notion of partial recursive functions can be found in the notes of "Computability Theory").

Notation: $\widetilde{\text{bin}}$

- ▶ When carrying out intermediate calculations, it is easier to refer to $\widetilde{\text{bin}}(n)$ rather than $\text{bin}(n)$
 - ▶ E.g. we can set a number on the tape easily to an element of $\widetilde{\text{bin}}(0)$ by overwriting it with 0s.
 - ▶ In order to set it to $\text{bin}(0)$ one would need to make sure that exactly one 0 remains. Then one usually has to shift left the content of the tape to the right of the original number.

Notation: $\widetilde{\text{bin}}$

- ▶ In this proof we will represent a configuration of a URM by a sequence of possibly non-normalised strings on the tape representing the registers.
- ▶ So we want to get a short notation for "The tape contains $s_0 \sqcup s_1 \sqcup s_2 \sqcup \dots \sqcup s_k$ where s_i is a binary representation of n_i " (where n_i is the simulated content of register R_i).
- ▶ We define $\widetilde{\text{bin}}(n)$ as one of the binary representations of n .
- ▶ Then we can write for the above: "The tape contains $\widetilde{\text{bin}}(n_0) \sqcup \widetilde{\text{bin}}(n_1) \sqcup \dots \sqcup \widetilde{\text{bin}}(n_k)$ ".
- ▶ So $\widetilde{\text{bin}}(n)$ denotes one of the possible choices for strings s s.t. $(s)_2 = n$.
 - ▶ So $\widetilde{\text{bin}}(1)$ can be "1", "01", "001", etc.
 - ▶ In the special case 0 we treat the empty string as one of the possible representations, so $\widetilde{\text{bin}}(0)$ can be "", "0", "00", "000", etc.

Proof of Lemma 3.4

Notation

The tape of a TM contains a_0, \dots, a_l means:

- ▶ Starting from the head position, the cells of the tape contain a_0, \dots, a_l .
- ▶ All other cells contain \sqcup .

Proof of Lemma 3.4

Assume

- ▶ $f = U^{(n)}$,
- ▶ U refers only to R_0, \dots, R_{l-1} and $l > n$,

We define a TM T , which simulates U . Done as follows:

- ▶ That the registers R_0, \dots, R_{l-1} contain a_0, \dots, a_{l-1} is simulated by the tape containing $\widetilde{\text{bin}}(a_0) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{l-1})$.
- ▶ An instruction I_j will be simulated by states $s_{j,0}, \dots, s_{j,i}$ with instructions for those states.

Conditions on the Simulation

- ▶ Assume the URM U is in a state s.t.
 - ▶ R_0, \dots, R_{l-1} contain a_0, \dots, a_{l-1} ,
 - ▶ the URM is about to execute I_j .
- ▶ Assume after executing I_j , the URM is in a state where
 - ▶ R_0, \dots, R_{l-1} contain b_0, \dots, b_{l-1} ,
 - ▶ the PC contains k .
- ▶ Then we want that, if configuration of the TM T is, s.t.
 - ▶ the tape contains $\widetilde{\text{bin}}(a_0) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{l-1})$,
 - ▶ and the TM is in state $s_{j,0}$,
- ▶ then the TM reaches a configuration s.t.
 - ▶ the tape contains $\widetilde{\text{bin}}(b_0) \sqcup \dots \sqcup \widetilde{\text{bin}}(b_{l-1})$,
 - ▶ the TM is in state $s_{k,0}$.

Example

- ▶ Assume the URM is about to execute instruction
 - ▶ $4 : R_2 := R_2 \div 1$ (i.e. PC = 4),
 - ▶ with register contents

R_0	R_1	R_2
2	1	3
- ▶ Then the URM will end with
 - ▶ PC = 5
 - ▶ and register contents

R_0	R_1	R_2
2	1	2

Example

- ▶ Then we want that, if the simulating TM is
 - ▶ in state $s_{4,0}$,
 - ▶ with tape content $\widetilde{\text{bin}}(2) \sqcup \widetilde{\text{bin}}(1) \sqcup \widetilde{\text{bin}}(3)$
- ▶ it should reach
 - ▶ state $s_{5,0}$
 - ▶ with tape content $\widetilde{\text{bin}}(2) \sqcup \widetilde{\text{bin}}(1) \sqcup \widetilde{\text{bin}}(2)$

Proof of Lemma 3.4

- Furthermore, we need initial states $s_{\text{init},0}, \dots, s_{\text{init},j}$ and corresponding instructions, s.t.

- if the TM initially contains

$$\widetilde{\text{bin}}(b_0) \sqcup \widetilde{\text{bin}}(b_1) \sqcup \dots \sqcup \widetilde{\text{bin}}(b_{n-1})$$

- it will reach state $s_{0,0}$ with the tape containing

$$\widetilde{\text{bin}}(b_0) \sqcup \widetilde{\text{bin}}(b_1) \sqcup \dots \sqcup \widetilde{\text{bin}}(b_{n-1}) \sqcup \underbrace{0 \sqcup 0 \sqcup \dots \sqcup 0}_{l-n \text{ times}}$$

Proof of Lemma 3.4

Then the corresponding TM will successively reach the following configurations:

State	Tape contains
$s_{\text{init},0}$	$\widetilde{\text{bin}}(a_0) \sqcup \widetilde{\text{bin}}(a_1) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{n-1}) \sqcup$
$s_{0,0}$	$\widetilde{\text{bin}}(a_0) \sqcup \widetilde{\text{bin}}(a_1) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{n-1}) \sqcup \widetilde{\text{bin}}(0) \sqcup \dots \sqcup \widetilde{\text{bin}}(0) \sqcup$
=	=
$s_{k_0,0}$	$\widetilde{\text{bin}}(a_{0,0}) \sqcup \widetilde{\text{bin}}(a_{0,1}) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{0,l-1}) \sqcup$
$s_{k_1,0}$	$\widetilde{\text{bin}}(a_{1,0}) \sqcup \widetilde{\text{bin}}(a_{1,1}) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{1,l-1}) \sqcup$
$s_{k_2,0}$	$\widetilde{\text{bin}}(a_{2,0}) \sqcup \widetilde{\text{bin}}(a_{2,1}) \sqcup \dots \sqcup \widetilde{\text{bin}}(a_{2,l-1}) \sqcup$
	...

Proof of Lemma 3.4

Assume the run of the URM, starting with R_i containing $a_{0,i} = a_i$ $i = 0, \dots, n-1$, and $a_{0,i} = 0$ for $i = n, \dots, l-1$ is as follows:

Instruction	R_0	R_1	\dots	R_{n-1}	R_n	\dots	R_{l-1}
0	a_0	a_1	\dots	a_{n-1}	0	\dots	0
=	=	=		=	=		=
k_0	$a_{0,0}$	$a_{0,1}$	\dots	$a_{0,n-1}$	$a_{0,n}$	\dots	$a_{0,l-1}$
k_1	$a_{1,0}$	$a_{1,1}$	\dots	$a_{1,n-1}$	$a_{1,n}$	\dots	$a_{1,l-1}$
k_2	$a_{2,0}$	$a_{2,1}$	\dots	$a_{2,n-1}$	$a_{2,n}$	\dots	$a_{2,l-1}$

Example

Consider the URM program U (which was discussed already in the section on URMs):

0 : if $R_0 = 0$ then goto 3

1 : $R_0 := R_0 \div 1$

2 : if $R_1 = 0$ then goto 0

$U^{(1)}(a) \simeq 0$.

Example

0 : if $R_0 = 0$ then goto 3
 1 : $R_0 := R_0 \div 1$
 2 : if $R_1 = 0$ then goto 0

We saw in the last section that a run of $U^{(1)}(2)$ is as follows:

Instruction	R_0	R_1
0	2	0
1	2	0
2	1	0
0	1	0
1	1	0
2	0	0
0	0	0
3	0	0

URM Stops

Corresponding TM Simulation

0 : if $R_0 = 0$ then goto 3
 1 : $R_0 := R_0 \div 1$
 2 : if $R_1 = 0$ then goto 0

Instruction	R_0	R_1	State of TM	Content of Tape
			$s_{\text{init},0}$	$\widetilde{\text{bin}}(2) _ _ _$
0	2	0	$s_{0,0}$	$\widetilde{\text{bin}}(2) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
1	2	0	$s_{1,0}$	$\widetilde{\text{bin}}(2) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
2	1	0	$s_{2,0}$	$\widetilde{\text{bin}}(1) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
0	1	0	$s_{0,0}$	$\widetilde{\text{bin}}(1) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
1	1	0	$s_{1,0}$	$\widetilde{\text{bin}}(1) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
2	0	0	$s_{2,0}$	$\widetilde{\text{bin}}(0) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
0	0	0	$s_{0,0}$	$\widetilde{\text{bin}}(0) _ _ _ \widetilde{\text{bin}}(0) _ _ _$
3	0	0	$s_{3,0}$	$\widetilde{\text{bin}}(0) _ _ _ \widetilde{\text{bin}}(0) _ _ _$

URM Stops

TM Stops

Proof of Lemma 3.4

If we have defined this we have

► If

$$U^{(n)}(a_0, \dots, a_{n-1}) \downarrow ,$$

$$U^{(n)}(a_0, \dots, a_{n-1}) \simeq c ,$$

then U eventually stops with R_i containing some values b_i , where $b_0 = c$.

Then, the TM T starting with

$$\text{bin}(a_0) _ _ _ \dots _ _ _ \text{bin}(a_{n-1})$$

will eventually terminate in a configuration

$$\widetilde{\text{bin}}(b_0) _ _ _ \dots _ _ _ \widetilde{\text{bin}}(b_{k-1}) .$$

for some $k \geq n$.

Therefore $T^{(n)}(a_0, \dots, a_{n-1}) \simeq b_0 = c$.

Proof of Lemma 3.4

► If

$$U^{(n)}(a_0, \dots, a_{n-1}) \uparrow ,$$

the URM U will loop and the TM T will carry out the same steps as the URM and loop as well.

Therefore

$$T^{(n)}(a_0, \dots, a_{n-1}) \uparrow ,$$

again

$$U^{(n)}(a_0, \dots, a_{n-1}) \simeq T^{(n)}(a_0, \dots, a_{n-1}) .$$

Proof of Lemma 3.4

- ▶ It follows

$$U^{(n)} = T^{(n)},$$

and the proof is complete, if the simulation has been introduced.

- ▶ The following slides contain a detailed proof, which will not be presented in the lecture this year.

[Jump over remaining proof.](#)

Proof of Lemma 3.4

- ▶ **Simulation of URM instructions.**

- ▶ **Simulation of instruction $k : R_j := R_j + 1$.**

Need to increase $(j + 1)$ st binary number by 1 Initial configuration:

$$\widetilde{\text{bin}}(c_0) \sqcup \widetilde{\text{bin}}(c_1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

$s_{k,0}$

- ▶ First move to the $(j + 1)$ st blank to the right. Then we are at the end of the $(j + 1)$ st binary number.

$$\widetilde{\text{bin}}(c_0) \sqcup \widetilde{\text{bin}}(c_1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

Proof of Lemma 3.4

Informal description of the simulation of URM instructions.

- ▶ **Initialisation.**

Initially, the tape contains $\text{bin}(a_0) \sqcup \cdots \sqcup \text{bin}(a_{n-1})$.

We need to obtain configuration:

$$\widetilde{\text{bin}}(a_0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(a_{n-1}) \sqcup \underbrace{\widetilde{\text{bin}}(0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(0)}_{l - n \text{ times}}$$

Achieved by

- ▶ moving head to the end of the initial configuration
- ▶ inserting, starting from the next blank, $l - n$ -times $0 \sqcup$,
- ▶ then moving back to the beginning.

Proof of Lemma 3.4

$$\widetilde{\text{bin}}(c_0) \sqcup \widetilde{\text{bin}}(c_1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

- ▶ Now perform the operation for increasing by 1 as above.

At the end we obtain:

$$\widetilde{\text{bin}}(c_0) \sqcup \widetilde{\text{bin}}(c_1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j + 1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

- ▶ It might be that we needed to write over the separating blank a 1, in which case we have:

$$\widetilde{\text{bin}}(c_0) \sqcup \widetilde{\text{bin}}(c_1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_{j-1}) \sqcup \widetilde{\text{bin}}(c_j + 1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

Proof of Lemma 3.4

- ▶ In the latter case, shift all symbols to the left once left, in order to obtain a separating \sqcup between the l th and $l - 1$ st entry.

We obtain

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \widetilde{\text{bin}}(c_{j-1}) & \sqcup & \widetilde{\text{bin}}(c_j + 1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) \\ \uparrow & & & & & & & & & & & & \end{array}$$

- ▶ Otherwise, move the head to the left, until we reach the $(j + 1)$ st blank to the left, and then move it once to the right.

We obtain

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_j + 1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) & \sqcup \\ \uparrow & & & & & & & & & & & & \end{array}$$

Proof of Lemma 3.4

- ▶ **Simulation of instruction $k : R_j := R_j \div 1$.**

- ▶ Assume the configuration at the beginning is :

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \widetilde{\text{bin}}(c_j) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) & \sqcup \\ \uparrow & & & & & & & & & & & \end{array}$$

We want to achieve

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \widetilde{\text{bin}}(c_j \div 1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) & \sqcup \\ \uparrow & & & & & & & & & & & \end{array}$$

Done as follows:

Proof of Lemma 3.4

$$\text{Initially: } \widetilde{\text{bin}}(c_0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

$$\text{Finally: } \widetilde{\text{bin}}(c_0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j \div 1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

- ▶ Move to end of the $(j + 1)$ st number.
- ▶ Check, if the number consists only of zeros or not.
 - ▶ If it consists only of zeros, $R_j := R_j \div 1$ doesn't change anything.
 - ▶ Otherwise, number is of the form $b_0 \cdots b_k 1 \underbrace{00 \cdots 0}_{l' \text{ times}}$.

Replace it by $b_0 \cdots b_k 0 \underbrace{11 \cdots 1}_{l' \text{ times}}$.

Done as for $R_j := R_j + 1$.

Proof of Lemma 3.4

$$\text{Initially: } \widetilde{\text{bin}}(c_0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

$$\text{Finally: } \widetilde{\text{bin}}(c_0) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_j \div 1) \sqcup \cdots \sqcup \widetilde{\text{bin}}(c_l) \sqcup$$

↑

- ▶ We have achieved

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \widetilde{\text{bin}}(c_j \div 1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) & \sqcup \\ & & & & & \uparrow & & & & & \end{array}$$

- ▶ Move back to the beginning:

$$\begin{array}{ccccccc} \widetilde{\text{bin}}(c_0) & \sqcup & \widetilde{\text{bin}}(c_1) & \sqcup & \cdots & \widetilde{\text{bin}}(c_j \div 1) & \sqcup & \cdots & \sqcup & \widetilde{\text{bin}}(c_l) & \sqcup \\ \uparrow & & & & & & & & & & \end{array}$$

Proof of Lemma 3.4

- ▶ **Simulation of instruction k** : if $R_j = 0$ then goto k' .
 - ▶ Move to $j + 1$ st binary number on the tape.
 - ▶ Check whether it contains only zeros.
 - ▶ If yes, switch to state $s_{k',0}$.
 - ▶ Otherwise switch to state $s_{k+1,0}$.

This completes the simulation of the URM U .

III.3 (a) Definition of the Turing Machine

III.3 (b) Equivalence of URM computable and Turing computable functions

III.3 (c) Undecidability of the Turing Halting Problem

III.4 The Church-Turing Thesis

Halting Problem with no Inputs

Theorem (3.8)

It is undecidable, whether a Turing machine started with a blank tape terminates.

▶ **Proof:**

- ▶ Let

$\text{Halt}'(e) :\Leftrightarrow e$ is a code for a Turing machine T
and T started with a blank tape
terminates

- ▶ Assume Halt' were decidable.

Halting Problem with no Inputs

- ▶ Then we can decide $\text{Halt}(e, n)$ as follows:

- ▶ Assume inputs e, n .
- ▶ If e is not a code for a Turing machine, we return 0.
- ▶ Otherwise, let $\text{encode}(T) = e$.
- ▶ Define a Turing machine V as follows:
 - ▶ V first writes $\text{bin}(n)$ on the tape and moves head to the left most bit of $\text{bin}(n)$.
 - ▶ Then it executes the Turing machine T .
- ▶ We have
 - ▶ V , run with blank tape, terminates
 - iff T run with tape containing $\text{bin}(n)$ terminates
 - iff $T^{(1)}(n) \downarrow$
 - iff $\{e\}(n) \downarrow$.

Halting Problem with no Inputs

V , run with blank tape, terminates iff $\{e\}(n)\downarrow$.

- ▶ Let $\text{encode}(V) = e'$. Then

$$\text{Halt}'(e') \Leftrightarrow \text{Halt}(e, n)$$

- ▶ Therefore using the decidability of Halt' we can decide $\text{Halt}(e, n)$.
- ▶ So we have decided Halt, a contradiction.

No Additional Material

For this subsection no additional material has been added yet.

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