There are two problems with the model of a URM:

Execution of a single URM instruction might take arbitrarily long:

Consider \( \text{succ}(n) \).

If \( R_n \) contains in binary \( 111 \cdots 111 \), this instruction replaces it by \( 1000 \cdots 000 \).

We have to replace \( k \) symbols 1 by 0.

\( k \) is arbitrary

\( \rightarrow \) this single step might take arbitrarily long time.
First Problem of URMs

That incrementing a number by one takes arbitrarily many steps happens on a real computer as well:

- If we want to represent arbitrary big numbers on the computer, we have to represent them by multiple machine integers
- Then incrementing a number by one will correspond to arbitrarily many machine instructions (although usually only a few).

However, often in complexity theory this problem is ignored because the effect is marginal in real applications.

- The exception are applications in which very big integers occur, e.g. tests for primality. There this effect cannot be ignored any more.
First Problem of URMs

If one takes this effect into account, one needs in many examples to multiply the running time by a factor of $\ln(n)$, where $n$ is the largest number occurring.

Therefore URMs unsuitable as a basis for defining the precise complexity of algorithms.

However, there are theorems linking complexity of URMs to actual complexities of algorithms.
Second Problem of URMs

- We aim at a notion of computability, which covers all possible ways of computing something, independently of any concrete machine.

- URMs are a model of computation which covers current standard computers.

- However, there might be completely different notions of computability, based on symbolic manipulations of a sequence of characters, where it might be more complicated to see directly that all such computations can be simulated by a URM.

- It is more easy to see that such notions are covered by the Turing machine model of computation.
Idea of a Turing Machine

Idea of a Turing machine (TM): Analysis of a computation carried out by a human being (agent) on a piece of paper.

\[
15 \times 16 = \\
15 \\
90 \\
240
\]
Idea of a Turing Machine

Steps in this formulation:

- Algorithm should be deterministic.
  → The agent will use only finitely many symbols, put at discrete positions on the paper.

\[
\begin{array}{cccc}
1 & 5 & . & 1 & 6 \\
\hline
1 & 5 \\
9 & 0 \\
- & - & - \\
2 & 4 & 0 \\
\end{array}
\]
Idea of a Turing Machine

- We can replace a two-dimensional piece of paper by one potentially infinite tape, by using a special symbol for a line break.
- Each entry on this tape is called a cell:

```
... 1 5 . 1 6 = CR 1 5 CR ...
```
Steps in Formalising TMs

In the real situation, agent can look at several cells at the same time, but bounded by his physical capability. Can be simulated by looking at one cell only at any time, and moving around in order to get information about neighbouring cells.

\[
\begin{array}{ccccccccc}
\cdots & 1 & 5 & . & 1 & 6 & = & CR & 1 & 5 & CR & \cdots \\
\end{array}
\]

↑

Head
Steps in Formalising TMs

In the real situation, an agent can make arbitrary jumps, but bounded by the physical ability of the agent. Each such jump can be simulated by finitely many one-step jumps.
→ Restriction to one-step movements.
Steps in Formalising TMs

In the real situation, an agent can make arbitrary jumps, but bounded by the physical ability of the agent. Each such jump can be simulated by finitely many one-step jumps.

→ Restriction to one-step movements.

\[
\begin{array}{cccccccc}
\cdots & 1 & 5 & . & 1 & 6 & = & \text{CR} \\
1 & 5 & \text{CR} & \cdots
\end{array}
\]

↑

Head
Steps in Formalising TMs

In the real situation, an agent can make arbitrary jumps, but bounded by the physical ability of the agent. Each such jump can be simulated by finitely many one-step jumps.

→ Restriction to one-step movements.
In the real situation, an agent can make arbitrary jumps, but bounded by the physical ability of the agent. Each such jump can be simulated by finitely many one-step jumps. → Restriction to one-step movements.

\[ \ldots 1\ 5\ .\ 1\ 6 = \ CR \ldots \]

↑

Head
Steps in Formalising TMs

In the real situation, an agent can make arbitrary jumps, but bounded by the physical ability of the agent. Each such jump can be simulated by finitely many one-step jumps.

→ Restriction to one-step movements.

\[
\cdots \ 1 \ 5 \ . \ 1 \ 6 \ = \ \text{CR} \ \cdots \\
\uparrow \ 1 \ 5 \ \text{CR} \ \cdots
\]

Head
Agent operates purely mechanistically: Reads a symbol, and depending on it changes it and makes a movement. Agent himself will have only finite memory.

→ There is a finite state of the agent, and, depending on the state and the symbol at the head, a next state, a new symbol, and a movement is chosen.
Steps in Formalising TMs

Agent operates purely mechanistically: Reads a symbol, and depending on it changes it and makes a movement. Agent himself will have only finite memory. → There is a finite state of the agent, and, depending on the state and the symbol at the head, a next state, a new symbol, and a movement is chosen.

\[
\begin{array}{cccccccccc}
\ldots & 1 & 5 & . & 1 & 6 & = & \text{CR} & \ldots \\
\uparrow & s_0 & 1 & 5 & \text{CR}
\end{array}
\]
Steps in Formalising TMs

Agent operates purely mechanistically:
Reads a symbol, and depending on it changes it and
makes a movement.
Agent himself will have only finite memory.
→ There is a finite state of the agent, and, depending
on the state and the symbol at the head, a next
state, a new symbol, and a movement is chosen.

\[ \cdots 1 \quad 5 \quad . \quad 0 \quad 6 = \quad \text{CR} \quad \cdots \]

\[ s_1 \]
Steps in Formalising TMs

Agent operates purely mechanistically: Reads a symbol, and depending on it changes it and makes a movement. Agent himself will have only finite memory.
→ There is a finite state of the agent, and, depending on the state and the symbol at the head, a next state, a new symbol, and a movement is chosen.

\[
\begin{array}{cccccccc}
\cdots & 1 & 5 & . & 0 & 7 & = & \text{CR} & \cdots \\
\uparrow \\
S_2
\end{array}
\]
Steps in Formalising TMs

Agent operates purely mechanistically:
Reads a symbol, and depending on it changes it and
makes a movement.
Agent himself will have only finite memory.

→ There is a finite state of the agent, and, depending
on the state and the symbol at the head, a next
state, a new symbol, and a movement is chosen.

\[
\begin{array}{cccccccc}
\cdots & 1 & 3 & . & 3 & 7 & = & \text{CR} & \cdots \\
\uparrow & & & & & & 1 & 5 & \text{CR} \\
& \text{s0} & & & & & & & \\
\end{array}
\]
Definition of TMs

A Turing machine is a quintuple \((\Sigma, S, I, \bot, s_0)\), where

- \(\Sigma\) is a finite set of symbols, called the alphabet of the Turing machine. On the tape, the symbols in \(\Sigma\) will be written.
- \(S\) is a finite set of states.
Definition of TMs

I is a finite sets of quintuples \((s, a, s', a', D)\), where
- \(s, s' \in S\),
- \(a, a' \in \Sigma\)
- \(D \in \{L, R\}\),

s.t. for every \(s \in S\), \(a \in \Sigma\) there is at most one \(s', a', D\) s.t. \((s, a, s', a', D) \in S\).

The elements of I are called **instructions**.

- \(
\sqcup
\)
  (a symbol for blank).
- \(s_0 \in S\) (the initial state).
Meaning of Instructions

A instruction \((s, a, s', a', D) \in I\) means the following:

- If the Turing machine is in state \(s\), and the symbol at position of the head is \(a\), then
  - the state is changed to \(s'\),
  - the symbol at this position is changed to \(a'\),
- if \(D = L\), the head moves left,
- if \(D = R\), the head moves right.
Meaning of Instructions

A instruction \((s, a, s', a', D) \in I\) means the following:

- If the Turing machine is in state \(s\), and the symbol at position of the head is \(a\), then
  - the state is changed to \(s'\),
  - the symbol at this position is changed to \(a'\),
  - if \(D = L\), the head moves left,
  - if \(D = R\), the head moves right.

Example:
\((s_0, 1, s_1, 0, R)\)
\((s_1, 6, s_2, 7, L)\)
Meaning of Instructions

A instruction \((s, a, s', a', D) \in I\) means the following:

- If the Turing machine is in state \(s\), and the symbol at position of the head is \(a\), then
- the state is changed to \(s'\),
- the symbol at this position is changed to \(a'\),
- if \(D = L\), the head moves left,
- if \(D = R\), the head moves moves right.

Example:

\[
\begin{align*}
(s_0, 1, s_1, 0, R) \\
(s_1, 6, s_2, 7, L)
\end{align*}
\]

\[
\begin{array}{cccccccc}
\cdots & 1 & 5 & . & 1 & 6 & = & \text{CR} & \cdots \\
\uparrow & & & & & & & & \text{s0}
\end{array}
\]
A instruction \((s, a, s', a', D) \in I\) means the following:

- If the Turing machine is in state \(s\), and the symbol at position of the head is \(a\), then
  - the state is changed to \(s'\),
  - the symbol at this position is changed to \(a'\),
  - if \(D = L\), the head moves left,
  - if \(D = R\), the head moves right.

**Example:**

\[
(s_0, 1, s_1, 0, R) \\
(s_1, 6, s_2, 7, L)
\]

\[
\begin{array}{cccccc|ccc}
\cdots & 1 & 5 & . & 0 & 6 & = & CR & & 1 & 5 & CR & \cdots \\
\end{array}
\]

\[
\uparrow
\]

\[
[\begin{array}{c} s_1 \end{array}]
\]
Meaning of Instructions

A instruction \((s, a, s', a', D) \in I\) means the following:
- If the Turing machine is in state \(s\), and the symbol at position of the head is \(a\), then
  - the state is changed to \(s'\),
  - the symbol at this position is changed to \(a'\),
- if \(D = L\), the head moves left,
- if \(D = R\), the head moves right.

Example:
\[(s_0, 1, s_1, 0, R)\]
\[(s_1, 6, s_2, 7, L)\]

\[
\begin{array}{cccc|c|c|c|c|c}
\cdots & 1 & 5 & . & 0 & 7 & = & & & & \text{CR} & \cdots \\
\hline
\end{array}
\]

\[
\uparrow
\]

\[
s_2
\]
Meaning of Instructions

Note that for the above it is important that for every \( s \in S, a \in \Sigma \) there is at most one \( s', a', D \) s.t. \((s, a, s', a', D) \in S\).

Without this condition, there might be more than one choice of selecting a new tape symbol, next state and direction.

If we omit this definition, we obtain a non-deterministic TM. In this case the machine selects in each step one of the possible choices (provided there exist one) at random.

If the Turing machine is in a state \( s \) and reads symbol \( a \) at his head, and there are no \( s', a', D \) s.t. \((s, a, s', a', D) \in I\), then the Turing machine stops.
A TM

\[(\Sigma, S, I, \bot, s_0)\]

can be visualised by a labelled graph as follows:

- Vertices: states (i.e. \(S\)).
- Edges: If \((s, a, t, b, D) \in I\), then there is an edge

Furthermore we write an arrow to the initial state coming from nowhere.

If there are several vertices from \(s\) to \(s'\), one draws only one arrow with one label for each vertex.
Example

The Turing machine with initial state \( s_0 \) and instructions

\[
\{(s_0, 0, s_0, 0, R), \\
(s_0, 1, s_0, 0, R), \\
(s_0, \_\_\_, s_1, \_\_\_, L), \\
(s_1, 0, s_1, 0, L), \\
(s_1, \_\_\_, s_2, \_\_\_, R)\}
\]

is visualised as follows (we write \( B \) instead of \( \_\_\_ \)):

- From state \( s_0 \):
  - \( 0/0, R \) leads to \( s_0 \) with \( 1/0, R \)
  - \( B/B, L \) leads to \( s_1 \) with \( B/B, R \)
- From state \( s_1 \):
  - \( 0/0, L \) leads to \( s_2 \)

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Example

- The TM on the previous slide sets the binary number the head is pointing to to zero, provided to the left of the head there are is a blank.

Exercise:

- This example assumes that the TM points to the left most digit of a binary number.
- Modify this TM, so that it works as well if the TM points initially to any digit of a binary number.
Example of a TM

- Development of a TM with $\Sigma = \{0, 1, \sqcup\}$,
  where $\sqcup$ is the symbol for the blank entry.

- Functionality of the TM:
  Assume initially the following:
  - The tape contains binary number,
  - The rest of the tape contains $\sqcup$.
  - The head points to any digit of the number.
  - The TM in state $s_0$.

  Then the TM stops after finitely many steps and then
  the tape contains the original number incremented by one,
  the rest of tape contains $\sqcup$,
  the head points to most significant bit.
Example

Initially

\[ \cdots \left[ \begin{array}{cccccccc} 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \cdots \\ \end{array} \right] \]

\[ \uparrow \]

\[ s_0 \]
Example

Initially

\[ \begin{array}{cccccccc}
\vdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \vdots \\
\end{array} \]

\[ s_0 \]

Finally

\[ \begin{array}{cccccccc}
\vdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & \vdots \\
\end{array} \]

\[ s_3 \]
Construction of the TM

- TM is \( \{0, 1, \perp\}, S, I, \perp, s_0 \).
- States \( S \) and instructions \( I \) developed in the following.
Step 1

- Initially, move head to least significant bit.
  - I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
  - If symbol is \[\sqcap\sqcup\], move head left, leave symbol again as it is.

- Achieved by the following instructions:
  
  \[
  (s_0, 0, s_0, 0, R) \\
  (s_0, 1, s_0, 1, R) \\
  (s_0, \sqcap\sqcup, s_1, \sqcap\sqcup, L)
  \]

- At the end TM is in state \(s_1\).
Step 1

Initially, move head to least significant bit.

- I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
- If symbol is \( \bot \), move head left, leave symbol again as it is.

Achieved by the following instructions:

\[
\begin{align*}
(s_0, 0, s_0, 0, R) \quad & \\
(s_0, 1, s_0, 1, R) \quad & \\
(s_0, \bot, s_1, \bot, L) \quad & 
\end{align*}
\]

At the end TM is in state \( s_1 \).

\[
\begin{array}{cccccccccccc}
\cdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \cdots \\
\end{array}
\]

\[
\begin{array}{c}
\text{↑} \\
S_0 \\
\end{array}
\]
Step 1

Initially, move head to least significant bit.

- I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
- If symbol is $\underline{\_\_}$, move head left, leave symbol again as it is.

Achieved by the following instructions:

\[(s_0, 0, s_0, 0, R)\]
\[(s_0, 1, s_0, 1, R)\]
\[(s_0, \underline{\_\_}, s_1, \underline{\_\_}, L)\]

At the end TM is in state $s_1$. 

\[
\begin{array}{cccccccccccc}
\cdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \cdots
\end{array}
\]
Step 1

Initially, move head to least significant bit.

I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.

If symbol is $\bot$, move head left, leave symbol again as it is.

Achieved by the following instructions:

- $(s_0, 0, s_0, 0, R)$
- $(s_0, 1, s_0, 1, R)$
- $(s_0, \bot, s_1, \bot, L)$

At the end TM is in state $s_1$. 

\[
\begin{array}{cccccccccccc}
\cdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \cdots \\
\end{array}
\]
Step 1

- Initially, move head to least significant bit.
- I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
- If symbol is \( \perp \perp \), move head left, leave symbol again as it is.

Achieved by the following instructions:

- \((s_0, 0, s_0, 0, R)\)
- \((s_0, 1, s_0, 1, R)\)
- \((s_0, \perp \perp, s_1, \perp \perp, L)\)

At the end TM is in state \(s_1\).
Step 1

Initially, move head to least significant bit.

I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.

If symbol is $\_|_\_|$, move head left, leave symbol again as it is.

Achieved by the following instructions:

$(s_0, 0, s_0, 0, R)$
$(s_0, 1, s_0, 1, R)$
$(s_0, \_|_\_, s_1, \_|_\_, L)$

At the end TM is in state $s_1$. 
Step 1

Initially, move head to least significant bit.

- I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
- If symbol is \( \bot \), move head left, leave symbol again as it is.

Achieved by the following instructions:

\[
(s_0, 0, s_0, 0, R) \\
(s_0, 1, s_0, 1, R) \\
(s_0, \bot, s_1, \bot, L)
\]

At the end TM is in state \( s_1 \).
Step 1

Initially, move head to least significant bit.
  I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
  If symbol is ⌞⌟, move head left, leave symbol again as it is.

Achieved by the following instructions:

\[(s_0, 0, s_0, 0, R)\]
\[(s_0, 1, s_0, 1, R)\]
\[(s_0, \_|\_|, s_1, \_|\_, L)\]

At the end TM is in state \(s_1\).

---

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Step 1

Initially, move head to least significant bit.

I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.

If symbol is \( \perp \), move head left, leave symbol again as it is.

Achieved by the following instructions:

\[
\begin{align*}
(s_0, 0, s_0, 0, R) \\
(s_0, 1, s_0, 1, R) \\
(s_0, \perp, s_1, \perp, L)
\end{align*}
\]

At the end TM is in state \( s_1 \).
Step 1

- Initially, move head to least significant bit.
  - I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
  - If symbol is \(\_
\), move head left, leave symbol again as it is.

Achieved by the following instructions:

\[
\begin{align*}
(s_0, 0, s_0, 0, R) \\
(s_0, 1, s_0, 1, R) \\
(s_0, \_
\, s_1, \_
, L)
\end{align*}
\]

- At the end TM is in state \(s_1\).
**Step 1**

- Initially, move head to least significant bit.
  - I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
  - If symbol is \(\sqcap\), move head left, leave symbol again as it is.

Achieved by the following instructions:

\[(s_0, 0, s_0, 0, R)\]
\[(s_0, 1, s_0, 1, R)\]
\[(s_0, \sqcap, s_1, \sqcap, L)\]

At the end TM is in state \(s_1\).
Step 1

- Initially, move head to least significant bit.
  - I.e. as long as symbol at head is 0 or 1, move right, leave symbol as it is.
  - If symbol is ⌞⌟, move head left, leave symbol again as it is.

- Achieved by the following instructions:
  - \((s_0, 0, s_0, 0, R)\)
  - \((s_0, 1, s_0, 1, R)\)
  - \((s_0, ⌞⌟, s_1, ⌞⌟, L)\)

- At the end TM is in state \(s_1\).
Step 2

Increasing a binary number $b$ done as follows:

- **Case number consists of 1 only:**

  - I.e. $b = (111 \cdots 111)_2$.
  - $b + 1 = (1000 \cdots 000)_2$.

  Obtained by replacing all ones by zeros and then replacing the first blank symbol by 1.
Step 2

Otherwise:

Then the representation of the number contains at the end one 0 followed by ones only. Includes case where the least significant digit is 0.

Example 1: \( b = (0100010111)_2 \), one 0 followed by 3 ones.

Example 2: \( b = (0100010010)_2 \), least significant digit is 0.

Let \( b = (b_0 b_1 \cdots b_k 0111 \cdots 111)_2 \).

\( b + 1 \) obtained by replacing the final block of ones by 0 and the 0 by 1:

\( b + 1 = (b_0 b_1 \cdots b_k 1000 \cdots 000)_2 \).
Step 2 – General Situation

- We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a $\perp\perp$, which is replaced by a 1.

- So we need a new state $s_2$, and the following instructions

  \[(s_1, 1, s_1, 0, L)\]
  \[(s_1, 0, s_2, 1, L)\]
  \[(s_1, \perp\perp, s_2, 1, L)\]

- At the end the head will be one field to the left of the 1 written, and the state will be $s_2$. 

\[\text{CS}_226 \text{ Computability Theory, Lent Term 2007, Sect. 4} \]
Step 2 – General Situation

We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a $\perp\perp$, which is replaced by a 1.

So we need a new state $s_2$, and the following instructions:

\[
\begin{align*}
(s_1, 1, s_1, 0, L) \\
(s_1, 0, s_2, 1, L) \\
(s_1, \perp\perp, s_2, 1, L)
\end{align*}
\]

At the end the head will be one field to the left of the 1 written, and the state will be $s_2$. 

\[
\begin{array}{cccccccccccc}
\ldots & | & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & \ldots \\
\uparrow & | & & | \\
S_1 & & & & & & & & & & & & &
\end{array}
\]
Step 2 – General Situation

- We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a \( \bot \bot \), which is replaced by a 1.

- So we need a new state \( s_2 \), and the following instructions

\[
(s_1, 1, s_1, 0, L) \\
(s_1, 0, s_2, 1, L) \\
(s_1, \bot \bot, s_2, 1, L)
\]

- At the end the head will be one field to the left of the 1 written, and the state will be \( s_2 \).
Step 2 – General Situation

- We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a \( \sqcup \sqcup \), which is replaced by a 1.

- So we need a new state \( s_2 \), and the following instructions

\[
\begin{align*}
(s_1, 1, s_1, 0, L) \\
(s_1, 0, s_2, 1, L) \\
(s_1, \sqcup \sqcup, s_2, 1, L)
\end{align*}
\]

- At the end the head will be one field to the left of the 1 written, and the state will be \( s_2 \).
Step 2 – General Situation

- We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a \( \bot \), which is replaced by a 1.

- So we need a new state \( s_2 \), and the following instructions

\[
\begin{align*}
&(s_1, 1, s_1, 0, L) \\
&(s_1, 0, s_2, 1, L) \\
&(s_1, \bot, s_2, 1, L)
\end{align*}
\]

- At the end the head will be one field to the left of the 1 written, and the state will be \( s_2 \).
Step 2 – General Situation

We have to replace, as long as we find ones, the ones by zeros, and move left, until we encounter a 0 or a \( \bot \), which is replaced by a 1.

So we need a new state \( s_2 \), and the following instructions:

\[
(s_1, 1, s_1, 0, \text{L}) \\
(s_1, 0, s_2, 1, \text{L}) \\
(s_1, \bot, s_2, 1, \text{L})
\]

At the end the head will be one field to the left of the 1 written, and the state will be \( s_2 \).

\[
\begin{array}{ccccccccccc}
\cdots & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & \cdots \\
\end{array}
\]
Step 3

Finally, we have to move the most significant bit, which is done as follows

$$(s_2, 0, s_2, 0, L)$$
$$(s_2, 1, s_2, 1, L)$$
$$(s_2, \text{--}, s_3, \text{--}, R)$$

The program terminates in state $s_3$. 
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \sqcup \sqcup, s_3, \sqcup \sqcup, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \bot, s_3, \bot, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \bot, s_3, \bot, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \bot, s_3, \bot, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \perp, s_3, \perp, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, ⊥, s_3, ⊥, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, \bot, s_3, \bot, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[(s_2, 0, s_2, 0, L)\]
\[(s_2, 1, s_2, 1, L)\]
\[(s_2, 	ext{| } 	ext{| }, s_3, 	ext{| } 	ext{| }, R)\]

The program terminates in state \(s_3\).
Step 3

Finally, we have to move the most significant bit, which is done as follows

\[ (s_2, 0, s_2, 0, L) \]
\[ (s_2, 1, s_2, 1, L) \]
\[ (s_2, \perp, s_3, \perp, R) \]

The program terminates in state \( s_3 \).
The complete TM is as follows:

\[
\begin{align*}
&\{0, 1, \bot\}, \\
&\{s_0, s_1, s_2, s_3\}, \\
&(s_0, 0, s_0, 0, R), \\
&(s_0, 1, s_0, 1, R), \\
&(s_0, \bot, s_1, \bot, L), \\
&(s_1, 1, s_1, 0, L), \\
&(s_1, 0, s_2, 1, L), \\
&(s_1, \bot, s_2, 1, L), \\
&(s_2, 0, s_2, 0, L), \\
&(s_2, 1, s_2, 1, L), \\
&(s_2, \bot, s_3, \bot, R)\}, \ s_0, \bot
\end{align*}
\]
Complete TM
Notation: \textit{bin}

- TMs usually operate on binary numbers.
- Therefore we define for a natural number \textit{bin}(n) as the sequence of digits representing the unique standard binary representation of \textit{n}.
- So \textit{bin}(n) has no leading zeros, except for \textit{bin}(0) := 0.

Examples:
- \textit{bin}(0) = 0,
- \textit{bin}(1) = 1,
- \textit{bin}(2) = 10,
- \textit{bin}(3) = 11,
- \textit{bin}(4) = 100, etc.
Notation: $\tilde{\text{bin}}$

- In order to read off the final result, we need to interpret an arbitrary finite sequence of 0, 1 as a binary number, even if it has leading zeros.

- We define $\tilde{\text{bin}}(n)$ as one of the possible binary representations of $n$, allowing leading 0.

  - So $\tilde{\text{bin}}(1)$ can be 1, 01, 001, etc.
  
  - In the special case 0 we treat the empty string as one of the possible representations, so $\tilde{\text{bin}}(0)$ contains the empty string, 0, 00, 000, etc.
Notation: $\tilde{\text{bin}}$

- When carrying out intermediate calculations, it is easier to refer to $\tilde{\text{bin}}(n)$ rather than $\text{bin}(n)$.

- E.g. we can set a number on the tape easily to $\tilde{\text{bin}}(0)$ by overwriting it with 0s.

- In order to set it to $\text{bin}(0)$ one would need to make sure that exactly one 0 remains. Then one usually has to shift left the content of the tape to the right of the original number.
Definition 4.1

Let $T = (\Sigma, S, I, \bot, s_0)$ be a Turing machine with $\{0, 1\} \subseteq \Sigma$. Define for every $k \in \mathbb{N}$ $\mathcal{T}^{(k)} : \mathbb{N}^k \xrightarrow{\sim} \mathbb{N}$, where $\mathcal{T}^{(k)}(a_0, \ldots, a_{k-1})$ is computed as follows:

- **Initialisation:**
  - The head is an arbitrary position.
  - Starting from this, we write
    $\text{bin}(a_0) \bot \text{bin}(a_1) \bot \cdots \bot \text{bin}(a_{k-1})$ on the tape.
    
    E.g. if $k = 3$, $a_0 = 0$, $a_1 = 3$, $a_2 = 2$ then we write
    $0 \bot 11 \bot 10$.
  - All other cells contain $\bot$.
  - The state is set to $s_0$.

- **Iteration:** Run the TM, until it stops.
Definition 4.1

Output:

**Case 1:** The TM stops.
Only finitely many cells are non-blank.
Let tape, starting from the head-position, contain $b_0 b_1 \cdots b_{k-1} c$ where $b_i \in \{0, 1\}$ and $c \not\in \{0, 1\}$.
($k$ might be 0).
Let $a = (b_0, \ldots, b_{k-1})_2$.
(in case $k = 0$, $a = 0$).
This means $b_0 \cdots b_{k-1}$ is one of the choices for $\tilde{\text{bin}}(a)$.
Then
\[ T^{(k)}(a_0, \ldots, a_{k-1}) \simeq a. \]
**Example:** Let \( \Sigma = \{0, 1, a, b, \bot\} \).

- If the tape starting with the head is as follows:
  - \(01010\bot0101\bot\)
  - or \(01010a\bot\),
  the output is \((01010)_2 = 10\).

- If tape starting with the head is as follows:
  - \(ab\bot\)
  - or \(a\),
  - or \(\bot\),
  the output is \(0\).

**Case 2:** Otherwise.
Then \(T^{(k)}(a_0, \ldots, a_{k-1})\uparrow\).
Definition 4.2

\[ f : \mathbb{N}^k \sim \mathbb{N} \text{ is Turing-computable, in short TM-computable, if } f = T^{(k)} \text{ for some TM } T, \text{ the alphabet of which contains } \{0, 1\}. \]

**Example:** That \( \text{succ} : \mathbb{N} \sim \mathbb{N} \) and \( \text{zero} : \mathbb{N} \sim \mathbb{N} \) are Turing-computable was shown above.
If the tape of the Turing machine initially contains only finitely many cells which are not blank, then at any step during the execution of the TM only finitely many cells are non blank.

Follows since in each step at most one cell can be modified to become non-blank.

So in finitely many steps only finitely many cells can be converted from blank to non-blank.
Theorem 4.3

If $f : \mathbb{N}^n \sim \mathbb{N}$ is URM-computable, then it is as well Turing-computable by a TM with alphabet $\{0, 1, \bot\}$. 
Proof of Theorem 4.3

Notation

The tape of a TM contains $a_0, \ldots, a_l$ means:

- Starting from the head position, the cells of the tape contain $a_0, \ldots, a_l$.
- All other cells contain $\blacksquare$. 
Proof of Theorem 4.3

Assume

- \( f = U^{(n)} \),
- \( U \) refers only to \( R_0, \ldots, R_{l-1} \) and \( l > n \),

We define a TM \( T \), which simulates \( U \). Done as follows:

- That the registers \( R_0, \ldots, R_{l-1} \) contain \( a_0, \ldots, a_{l-1} \) is simulated by the tape containing \( \widetilde{\text{bin}}(a_0) \_ \cdot \cdot \cdot \_ \_ \_ \_ \_ \_ \_ \text{bin}(a_{l-1}) \).
- An instruction \( I_j \) will be simulated by states \( s_{j,0}, \ldots, s_{j,i} \) with instructions for those states.
Conditions on the Simulation

- Assume the URM \( U \) is in a state s.t.
  - \( R_0, \ldots, R_{l-1} \) contain \( a_0, \ldots, a_{l-1} \),
  - the URM is about to execute \( I_j \).

- Assume after executing \( I_j \), the URM is in a state where
  - \( R_0, \ldots, R_{l-1} \) contain \( b_0, \ldots, b_{l-1} \),
  - the PC contains \( k \).

Then we want that, if configuration of the TM \( T \) is, s.t.
- the tape contains \( \tilde{\text{bin}}(a_0) \mid \ldots \mid \tilde{\text{bin}}(a_{l-1}) \),
  - and the TM is in state \( s_{j,0} \),

then the TM reaches a configuration s.t.
- the tape contains \( \tilde{\text{bin}}(b_0) \mid \ldots \mid \tilde{\text{bin}}(b_{l-1}) \),
  - the TM is in state \( s_{k,0} \).
Example

Assume the URM is about to execute instruction
\[ I_4 = \text{pred}(2) \] (i.e. PC = 4),
with register contents

<table>
<thead>
<tr>
<th>R₀</th>
<th>R₁</th>
<th>R₂</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>1</td>
<td>3</td>
</tr>
</tbody>
</table>

Then the URM will end with
<table>
<thead>
<tr>
<th>PC = 5</th>
</tr>
</thead>
</table>

and register contents

<table>
<thead>
<tr>
<th>R₀</th>
<th>R₁</th>
<th>R₂</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>1</td>
<td>2</td>
</tr>
</tbody>
</table>
Example

Then we want that, if the simulating TM is
in state \( s_{4,0} \),
with tape content \( \tilde{\text{bin}}(2) \underline{l} \text{bin}(1) \underline{l} \text{bin}(3) \)
it should reach
state \( s_{5,0} \)
with tape content \( \tilde{\text{bin}}(2) \underline{l} \text{bin}(1) \underline{l} \text{bin}(2) \)
Proof of Theorem 4.3

Furthermore, we need initial states $s_{\text{init},0}, \ldots, s_{\text{init},j}$ and corresponding instructions, s.t.

if the TM initially contains

$$\widetilde{\text{bin}}(b_0)\underline{\ldots}\underline{\text{bin}}(b_1)\underline{\ldots}\underline{\text{bin}}(b_{n-1})$$

it will reach state $s_{0,0}$ with the tape containing

$$\widetilde{\text{bin}}(b_0)\underline{\ldots}\underline{\text{bin}}(b_1)\underline{\ldots}\underline{\text{bin}}(b_{n-1})\underline{\ldots}\underline{0}\underline{\ldots}\underline{0}\ldots\underline{0}\underline{\ldots}\underline{0}\ldots\underline{0}$$

$l - n$ times
Proof of Theorem 4.3

Assume the run of the URM, starting with $R_i$ containing $a_{0,i} = a_i$ for $i = 0, \ldots, n - 1$, and $a_{0,i} = 0$ for $i = n, \ldots, l - 1$ is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>$R_0$</th>
<th>$R_1$</th>
<th>$\cdots$</th>
<th>$R_{n-1}$</th>
<th>$R_n$</th>
<th>$\cdots$</th>
<th>$R_{l-1}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_0$</td>
<td>$a_0$</td>
<td>$a_1$</td>
<td>$\cdots$</td>
<td>$a_{n-1}$</td>
<td>$0$</td>
<td>$\cdots$</td>
<td>$0$</td>
</tr>
<tr>
<td>$=</td>
<td>$=$</td>
<td>$=$</td>
<td>$\cdots$</td>
<td>$=$</td>
<td>$=$</td>
<td>$\cdots$</td>
<td>$=$</td>
</tr>
<tr>
<td>$I_{k_0}$</td>
<td>$a_{0,0}$</td>
<td>$a_{0,1}$</td>
<td>$\cdots$</td>
<td>$a_{0,n-1}$</td>
<td>$a_{0,n}$</td>
<td>$\cdots$</td>
<td>$a_{0,l-1}$</td>
</tr>
<tr>
<td>$I_{k_1}$</td>
<td>$a_{1,0}$</td>
<td>$a_{1,1}$</td>
<td>$\cdots$</td>
<td>$a_{1,n-1}$</td>
<td>$a_{1,n}$</td>
<td>$\cdots$</td>
<td>$a_{1,l-1}$</td>
</tr>
<tr>
<td>$I_{k_2}$</td>
<td>$a_{2,0}$</td>
<td>$a_{2,1}$</td>
<td>$\cdots$</td>
<td>$a_{2,n-1}$</td>
<td>$a_{2,n}$</td>
<td>$\cdots$</td>
<td>$a_{2,l-1}$</td>
</tr>
<tr>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
<td>$\cdots$</td>
</tr>
</tbody>
</table>
Proof of Theorem 4.3

Then the corresponding TM will successively reach the following configurations:

<table>
<thead>
<tr>
<th>State</th>
<th>Tape contains</th>
</tr>
</thead>
<tbody>
<tr>
<td>$s_{\text{init},0}$</td>
<td>$\overline{\text{bin}}(a_0)\overline{\text{bin}}(a_1)\overline{\text{bin}}(a_{n-1}),\ldots,$</td>
</tr>
<tr>
<td>$s_{0,0}$</td>
<td>$\overline{\text{bin}}(a_0)\overline{\text{bin}}(a_1)\overline{\text{bin}}(a_{n-1})\overline{\text{bin}}(0),\ldots,$</td>
</tr>
<tr>
<td>$=\quad$</td>
<td>$\overline{\text{bin}}(a_0,0)\overline{\text{bin}}(a_{0,1})\overline{\text{bin}}(a_{0,l-1}),\ldots,$</td>
</tr>
<tr>
<td>$s_{k_0,0}$</td>
<td>$\overline{\text{bin}}(a_{0,0})\overline{\text{bin}}(a_{0,1})\overline{\text{bin}}(a_{0,l-1}),\ldots,$</td>
</tr>
<tr>
<td>$s_{k_1,0}$</td>
<td>$\overline{\text{bin}}(a_{1,0})\overline{\text{bin}}(a_{1,1})\overline{\text{bin}}(a_{1,l-1}),\ldots,$</td>
</tr>
<tr>
<td>$s_{k_2,0}$</td>
<td>$\overline{\text{bin}}(a_{2,0})\overline{\text{bin}}(a_{2,1})\overline{\text{bin}}(a_{2,l-1}),\ldots,$</td>
</tr>
<tr>
<td></td>
<td>$\ldots$</td>
</tr>
</tbody>
</table>
Example

Consider the URM program $U$:

$I_0 = \text{ifzero}(0, 3)$
$I_1 = \text{pred}(0)$
$I_2 = \text{ifzero}(1, 0)$

$U^{(1)}(a) \simeq 0$. 
Example

$I_0 = \text{ifzero}(0, 3)\n$I_1 = \text{pred}(0)\n$I_2 = \text{ifzero}(1, 0)\n
Example

$I_0 = \text{ifzero}(0, 3)$
$I_1 = \text{pred}(0)$
$I_2 = \text{ifzero}(1, 0)$

A run of $U^{(1)}(2)$ is as follows:
Example

\[
\begin{align*}
I_0 & = \text{ifzero}(0, 3) \\
I_1 & = \text{pred}(0) \\
I_2 & = \text{ifzero}(1, 0)
\end{align*}
\]

A run of \( U^{(1)}(2) \) is as follows:

Instruction \( R_0 \quad R_1 \)
Example

\[ I_0 = \text{ifzero}(0, 3) \]
\[ I_1 = \text{pred}(0) \]
\[ I_2 = \text{ifzero}(1, 0) \]

A run of \( U^{(1)}(2) \) is as follows:

<table>
<thead>
<tr>
<th>Instruction</th>
<th>( R_0 )</th>
<th>( R_1 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_0 )</td>
<td>2</td>
<td>0</td>
</tr>
</tbody>
</table>
Example

\[ I_0 = \text{ifzero}(0, 3) \]
\[ I_1 = \text{pred}(0) \]
\[ I_2 = \text{ifzero}(1, 0) \]

A run of \( U^{(1)}(2) \) is as follows:

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Example

\[ I_0 = \text{ifzero}(0, 3) \]
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<tr>
<td>( I_2 )</td>
<td>1</td>
<td>0</td>
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Example

\[ I_0 = \text{ifzero}(0, 3) \]
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<tr>
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Example

\[ I_0 = \text{ifzero}(0, 3) \]
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</tr>
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<td>0</td>
</tr>
<tr>
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Example

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<td>( I_2 )</td>
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</tr>
<tr>
<td>( I_0 )</td>
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<td>0</td>
</tr>
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Example

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<td>2</td>
<td>0</td>
</tr>
<tr>
<td>I₁</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>I₂</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>I₀</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>I₁</td>
<td>1</td>
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</tr>
<tr>
<td>I₂</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>I₀</td>
<td>0</td>
<td>0</td>
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Example

\[ I_0 = \text{ifzero}(0, 3) \]
\[ I_1 = \text{pred}(0) \]
\[ I_2 = \text{ifzero}(1, 0) \]

A run of \( U^{(1)}(2) \) is as follows:

<table>
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<tr>
<th>Instruction</th>
<th>R₀</th>
<th>R₁</th>
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<tr>
<td>I₀</td>
<td>2</td>
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</tr>
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<td>0</td>
</tr>
<tr>
<td>I₀</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>I₁</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>I₂</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>I₀</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>I₃</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>
Example

$I_0 = \text{ifzero}(0, 3)$
$I_1 = \text{pred}(0)$
$I_2 = \text{ifzero}(1, 0)$

A run of $U^{(1)}(2)$ is as follows:

<table>
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<tr>
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<th>$R_0$</th>
<th>$R_1$</th>
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</thead>
<tbody>
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<td>$I_0$</td>
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</tr>
<tr>
<td>$I_1$</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>$I_2$</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>$I_0$</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>$I_0$</td>
<td>1</td>
<td>0</td>
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<tr>
<td>$I_1$</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>$I_2$</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>$I_0$</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>$I_3$</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

URM Stops
## Corresponding TM Simulation

\[ I_0 = \text{ifzero}(0,3) \]
\[ I_1 = \text{pred}(0) \]
\[ I_2 = \text{ifzero}(1,0) \]

<table>
<thead>
<tr>
<th>Instruction</th>
<th>( R_0 )</th>
<th>( R_1 )</th>
<th>State of TM</th>
<th>Content of Tape</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_0 )</td>
<td>2</td>
<td>0</td>
<td>( s_{\text{init},0} )</td>
<td>( \text{bin}(2) )</td>
</tr>
<tr>
<td>( I_1 )</td>
<td>2</td>
<td>0</td>
<td>( s_{0,0} )</td>
<td>( \text{bin}(2) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_2 )</td>
<td>1</td>
<td>0</td>
<td>( s_{1,0} )</td>
<td>( \text{bin}(2) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_0 )</td>
<td>1</td>
<td>0</td>
<td>( s_{2,0} )</td>
<td>( \text{bin}(1) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_1 )</td>
<td>1</td>
<td>0</td>
<td>( s_{0,0} )</td>
<td>( \text{bin}(1) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_2 )</td>
<td>0</td>
<td>0</td>
<td>( s_{1,0} )</td>
<td>( \text{bin}(1) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_2 )</td>
<td>0</td>
<td>0</td>
<td>( s_{2,0} )</td>
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<td>( s_{0,0} )</td>
<td>( \text{bin}(0) \text{bin}(0) )</td>
</tr>
<tr>
<td>( I_3 )</td>
<td>0</td>
<td>0</td>
<td>( s_{3,0} )</td>
<td>( \text{bin}(0) \text{bin}(0) )</td>
</tr>
</tbody>
</table>

URM Stops | TM Stops
Proof of Theorem 4.3

If we have defined this we have

If

$$U^n(a_0, \ldots, a_{n-1}) \downarrow,$$

$$U^n(a_0, \ldots, a_{n-1}) \simeq c,$$

then $U$ eventually stops with $R_i$ containing some values $b_i$, where $b_0 = c$.

Then, the TM $T$ starting with

$$\text{bin}(a_0) \ll \cdots \ll \text{bin}(a_{n-1})$$

will eventually terminate in a configuration

$$\tilde{\text{bin}}(b_0) \ll \cdots \ll \tilde{\text{bin}}(b_{k-1}) .$$

for some $k \geq n$.

Therefore $T^n(a_0, \ldots, a_{n-1}) \simeq b_0 = c$. 
Proof of Theorem 4.3

If

\[ U^n(a_0, \ldots, a_{n-1}) \uparrow, \]

the URM \( U \) will loop and the TM \( T \) will carry out the same steps as the URM and loop as well. Therefore

\[ T^n(a_0, \ldots, a_{n-1}) \uparrow, \]

again

\[ U^n(a_0, \ldots, a_{n-1}) \simeq T^n(a_0, \ldots, a_{n-1}). \]
Proof of Theorem 4.3

It follows

\[ U(n) = T(n) \]

and the proof is complete, if the simulation has been introduced.

The following slides contain a detailed proof, which will not be presented in the lecture this year.

Jump over remaining proof.
Proof of Theorem 4.3

Informal description of the simulation of URM instructions.

- **Initialisation.**

  Initially, the tape contains \( \text{bin}(a_0) \ldots \text{bin}(a_{n-1}) \).

  We need to obtain configuration:

  \[
  \text{bin}(a_0) \ldots \text{bin}(a_{n-1}) \text{bin}(0) \ldots \text{bin}(0).
  \]

  Achieved by

  - moving head to the end of the initial configuration
  - inserting, starting from the next blank, \( l - n \)-times \( 0 \),
  - then moving back to the beginning.
Proof of Theorem 4.3

- Simulation of URM instructions.
  - **Simulation of instruction** $I_k = \text{succ}(j)$.

Need to increase $(j + 1)$st binary number by 1

Initial configuration:

$$\text{bin}(c_0) \quad \_\_ \quad \text{bin}(c_1) \quad \_\_ \quad \ldots \quad \_\_ \quad \text{bin}(c_j) \quad \_\_ \quad \ldots \quad \_\_ \quad \text{bin}(c_l) \quad \_\_ \quad \uparrow$$

$s_{k, 0}$

First move to the $(j + 1)$st blank to the right. Then we are at the end of the $(j + 1)$st binary number.

$$\text{bin}(c_0) \quad \_\_ \quad \text{bin}(c_1) \quad \_\_ \quad \ldots \quad \_\_ \quad \text{bin}(c_j) \quad \_\_ \quad \ldots \quad \_\_ \quad \text{bin}(c_l) \quad \_\_ \quad \uparrow$$
Proof of Theorem 4.3

Now perform the operation for increasing by 1 as above.
At the end we obtain:

\[ \overline{\text{bin}}(c_0) \quad \overline{\text{bin}}(c_1) \quad \ldots \quad \overline{\text{bin}}(c_j) \quad \ldots \quad \overline{\text{bin}}(c_l) \quad \uparrow \]

It might be that we needed to write over the separating blank a 1, in which case we have:

\[ \overline{\text{bin}}(c_0) \quad \overline{\text{bin}}(c_1) \quad \ldots \quad \overline{\text{bin}}(c_{j-1}) \quad \overline{\text{bin}}(c_j + 1) \quad \ldots \quad \overline{\text{bin}}(c_l) \quad \uparrow \]
Proof of Theorem 4.3

In the latter case, shift all symbols to the left once left, in order to obtain a separating \( \square \) between the \( l \)th and \( l - 1 \)st entry.

We obtain

\[
\tilde{\text{bin}}(c_0) \quad \square \quad \tilde{\text{bin}}(c_1) \quad \square \quad \cdots \quad \tilde{\text{bin}}(c_{j-1}) \quad \square \quad \tilde{\text{bin}}(c_j + 1) \quad \square \quad \cdots \quad \square \quad \uparrow
\]

Otherwise, move the head to the left, until we reach the \((j + 1)\)st blank to the left, and then move it once to the right.

We obtain

\[
\tilde{\text{bin}}(c_0) \quad \square \quad \tilde{\text{bin}}(c_1) \quad \square \quad \cdots \quad \square \quad \tilde{\text{bin}}(c_j + 1) \quad \square \quad \cdots \quad \square \quad \tilde{\text{bin}}(c_l) \quad \uparrow
\]
Proof of Theorem 4.3

Simulation of instruction \( I_k = \text{pred}(j) \).

Assume the configuration at the beginning is:

\[
\begin{array}{cccccccc}
\text{bin}(c_0) & \downarrow & \text{bin}(c_1) & \downarrow & \cdots & \text{bin}(c_j) & \downarrow & \cdots & \downarrow & \text{bin}(c_l) & \downarrow \\
\uparrow
\end{array}
\]

We want to achieve

\[
\begin{array}{cccccccc}
\text{bin}(c_0) & \downarrow & \text{bin}(c_1) & \downarrow & \cdots & \text{bin}(c_j - 1) & \downarrow & \cdots & \downarrow & \text{bin}(c_l) & \downarrow \\
\uparrow
\end{array}
\]

Done as follows:
Proof of Theorem 4.3

Initially: \( \tilde{\text{bin}}(c_0) \uparrow \cdots \uparrow \tilde{\text{bin}}(c_j) \uparrow \cdots \uparrow \tilde{\text{bin}}(c_l) \uparrow \)

\[ \uparrow \]

Finally: \( \tilde{\text{bin}}(c_0) \uparrow \cdots \uparrow \tilde{\text{bin}}(c_j-1) \uparrow \cdots \uparrow \tilde{\text{bin}}(c_l) \uparrow \)

\[ \uparrow \]

Move to end of the \((j+1)\)st number.

Check, if the number consists only of zeros or not.

If it consists only of zeros, \( \text{pred}(j) \) doesn’t change anything.

Otherwise, number is of the form \( b_0 \cdots b_k 1 00 \cdots 0 \). \( l' \) times

Replace it by \( b_0 \cdots b_k 0 11 \cdots 1 \). \( l' \) times

Done as for \( \text{succ} \).
Proof of Theorem 4.3

Initially: \( \tilde{\text{bin}}(c_0) \quad \cdots \quad \tilde{\text{bin}}(c_j) \quad \cdots \quad \tilde{\text{bin}}(c_l) \quad \uparrow \)

Finally: \( \tilde{\text{bin}}(c_0) \quad \cdots \quad \tilde{\text{bin}}(c_j-1) \quad \cdots \quad \tilde{\text{bin}}(c_l) \quad \uparrow \)

We have achieved

\( \tilde{\text{bin}}(c_0) \quad \cdots \quad \tilde{\text{bin}}(c_j-1) \quad \cdots \quad \tilde{\text{bin}}(c_l) \quad \uparrow \)

Move back to the beginning:

\( \tilde{\text{bin}}(c_0) \quad \cdots \quad \tilde{\text{bin}}(c_j-1) \quad \cdots \quad \tilde{\text{bin}}(c_l) \quad \uparrow \)
Proof of Theorem 4.3

Simulation of instruction $I_k = \text{ifzero}(j, k')$.

- Move to $j + 1$st binary number on the tape.
- Check whether it contains only zeros.
  - If yes, switch to state $s_{k',0}$.
  - Otherwise switch to state $s_{k+1,0}$.

This completes the simulation of the URM $U$. 
Remark

We will later show that all TM-computable functions are URM-computable.

This will be done by showing that all TM-computable functions are partial recursive, all partial recursive functions are URM-computable.

This will be easier than showing directly that TM-computable functions are URM-computable.

Therefore the set of TM-computable functions and the set of URM-computable functions coincide.
Extension to Arbitrary Alphabets

Let \( A \) be a finite alphabet s.t. \( \bot \notin A \), and \( B := A^* \).

To a Turing machine \( T = (\Sigma, S, I, \bot, s_0) \) with \( A \subseteq \Sigma \) corresponds a partial function \( T^{(A,n)} : B^n \rightarrow B \), where \( T^{(A,n)}(a_0, \ldots, a_{n-1}) \) is computed as follows:

- Initially write \( a_0 \bot \cdots \bot a_{n-1} \) on the tape, otherwise \( \bot \).

- Start in state \( s_0 \) on the left most position of \( a_0 \).

- Iterate TM as before.

- In case of termination, the output of the function is \( c_0 \cdots c_{l-1} \), if the tape contains, starting with the head position \( c_0 \cdots c_{l-1}d \) with \( c_i \in A, \ d \notin A \).

- Otherwise, the function value is undefined.
Extension to Arbitrary Alphabets

Notion is modulo encoding of $A^*$ into $\mathbb{N}$ equivalent to the notion of Turing-computability on $\mathbb{N}$.

However, when considering complexity bounds, this notation might be more appropriate.

Avoids encoding/decoding into $\mathbb{N}$. 
Turing-Computable Predicates

- A predicate $A$ is Turing-decidable, iff $\chi_A$ is Turing-computable.

- Instead of simulating $\chi_A$
  means to write the output of $\chi_A$ (a binary number 0 or 1) on the tape
  it is more convenient, to take TM with two additional special states $s_{\text{true}}$ and $s_{\text{false}}$ corresponding to truth and falsity of the predicate.
Turing-Computable Predicates

Then a predicate is Turing decidable, if, when we write initially the inputs as before on the tape and start executing the TM,

- it always terminates in $s_{\text{true}}$ or $s_{\text{false}}$,
- and it terminates in $s_{\text{true}}$, iff the predicate holds for the inputs,
- and in $s_{\text{false}}$, otherwise.

The latter notion is equivalent to the first notion.

Usually the latter one is taken as basis for complexity considerations.