

AVoCS 2009 - Final Programme

23.-25. September 2009

Wednesday, 23.9.

Lunch 12.30 – 13.30

Full Papers – Session 1 14.00 - 15.30

Welcome Address

Nathaniel Charlton and Bernhard Reus:

A decidable class of verification conditions for programs with higher order store

Holger Gast and Julia Trieflinger:

High-level Proofs about Low-level Programs

Reynald Affeldt, David Nowak and Kiyoshi Yamada:

Certifying Assembly with Formal Cryptographic Proofs: the Case of BBS

Coffee Break

Full Papers – Session 2 16.00 - 17.30

Julio C. Peralta and Thierry Gautier:

Towards SMV Model Checking of SIGNAL (multi-clocked) Specifications

Étienne André, Laurent Fribourg, and Jeremy Sproston:

An Extension of the Inverse Method to Probabilistic Timed Automata

L. Besnard, T. Gautier, M. Moy, J.-P. Talpin, K. Johnson and F. Maraninchi:

Automatic translation of C/C++ parallel code into synchronous formalism using an SSA intermediate form

Break

Short Contributions – Session 1 18.00 - 18.40

Shamim H. Ripon, Alice Miller and Alastair F. Donaldson:

A Semantic Embedding of Promela-Lite in PVS

Anaheed Ayoub, Ayman Wahba, Ashraf Salem and Mohamed Sheirah:

B Based Verification for Real Time Systems

Welsh Dinner 19.00 - 20.00

Notturmo 20.30 – 21.00

Music with Marya Vrba, violin, and Markus Roggenbach, piano

Bar opens at 21.00

Thursday, 24.9.

Breakfast 8.00 – 9.00

Invited Speaker 9.00 - 10.00

Christoph Lüth:

User Interfaces for Theorem Provers: Necessary Nuisance or Unexplored Potential?

Coffee Break

Full Papers – Session 3 10.30 - 12.00

H. Palikareva, J. Ouaknine and A. W. Roscoe:

Faster FDR Counterexample Generation Using SAT-Solving

Neil C. C. Brown:

Automatically Generating CSP Models for Communicating Haskell Processes

F.P.M. Stappers and M.A. Reniers:

Verification of safety requirements for program code using data abstraction

Lunch 12.30 – 13.30

Time for a walk around the Gregynog Grounds

Short Contributions – Session 2 15.00 - 16.20

C.-H. L. Ong and N. Tzevelekos:

Functional Reachability

Monika Seisenberger:

Program Verification via Extraction from Coinductive Proofs

Alexei Lisitsa:

Reachability as deducibility, finite countermodels and verification

Karim Kanso and Anton Setzer:

Specifying Railway Interlocking Systems

Coffee Break

Short Contributions – Session 3 17.00 - 18.00

Savas Konur, Ahmed Al Zahrani and Michael Fisher:

Verification of a Message Delivery System using PRISM

Nikolaos Papanikolaou, Sadie Creese and Michael Goldsmith:

Policy Refinement Checking

Alan Bundy, Gudmund Grov and Cliff B. Jones:

Learning from experts to aid the automation of proof search

Workshop Dinner 19.00

Bar opens at 21.00

Friday, 25.9.

Breakfast 8.00 – 9.00

Invited Speaker 9.00 - 10.00

Ulrich Berger:

Proofs-as-Programs in Computable Analysis

Coffee Break

Full Papers – Session 4 10.30 - 12.00

Michael Huth, Nir Piterman and Huaxin Wang:

A workbench for preprocessor design and evaluation: toward benchmarks for parity games

Ulrich Berger and Sion Lloyd:

A coinductive approach to verified exact real number computation

Beeta Vajar, Steve Schneider and Helen Treharne:

Mobile CSP||B

Workshop closing / Invitation to the next AVoCS

Lunch 12.30 – 13.30